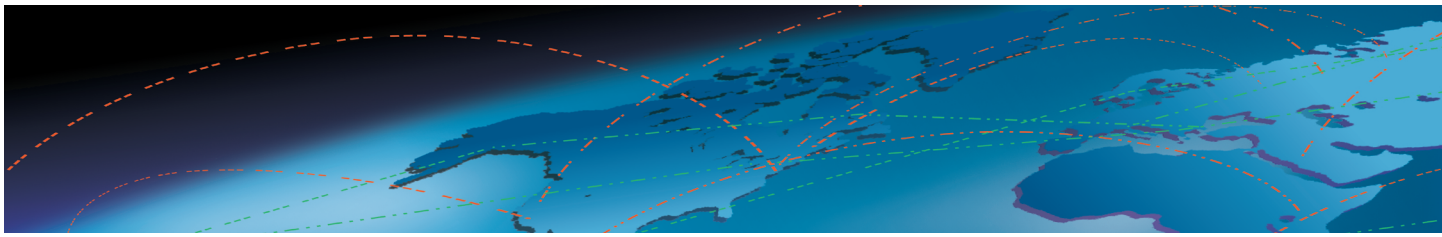

AD808

622 Mbps Limiting Amplifier and Clock and Data Recovery IC

HIGH LEVEL OF PERFORMANCE AND FUNCTIONAL INTEGRATION FACILITATES DESIGN WHILE MINIMIZING MANUFACTURING COSTS

As the demand for bandwidth continues to grow, Analog Devices is focused on providing high-performance yet cost-effective interface IC solutions to address the needs of today's optical networking systems.

The AD808 is a 622 Mbps clock and data recovery IC with an integrated post-amplifier function. No external reference clocks are required. All SONET/SDH jitter specifications are exceeded, including jitter tolerance, jitter transfer, and jitter generation.

With a substantial level of integration and superior jitter performance, Analog's AD808 is industry-proven for use in OC-12/SDH-4 fiber optic receivers.

Analog has over 10 years of experience in designing fiber optic components; put our expertise to work for you.

The AD808 leverages Analog's design expertise for 622 Mbps fiber optic receiver applications.

FEATURES:

- Integrated Limiting Amplifier: 4 mV Sensitivity, Typical
- No External Reference Clock or Crystal Required
- Exceed all SONET/SDH Requirements for Jitter Transfer, Jitter Generation, and Jitter Tolerance
- Low Power: 400 mW
- Single Supply: 5 V
- Loss-of-Signal Alarm

ANALOG DEVICES

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